

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	((memory or storage) and (sram or static) and discharg\$5 near4 (dummy or reference) adj3 (bit or bitline) and equaliz\$5 and (low\$5 below) and (power or supply or voltage)).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 19:19
L2	1	((memory or storage) and (sram or static or (six or "6") adj3 transistor) and discharg\$5 near4 (dummy or reference) adj3 (bit or bitline) and equaliz\$5 and (low\$5 below) and (power or supply or voltage)).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 19:20
L3	362	((memory or storage) and (sram or static or (six or "6") adj3 transistor) and (word or wordline or row) and (below or low\$5) near4 (power or supply or voltage)).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 19:22
L4	150	((memory or storage) and (sram or static or (six or "6") adj3 transistor) and (activat\$5 or select\$5) near3 (word or wordline or row) and (below or low\$5) near4 (power or supply or voltage)).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 19:23
L5	3	((memory or storage) and (sram or static or (six or "6") adj3 transistor) and (activat\$5 or select\$5) near3 (word or wordline or row) and (below or low\$5) near4 (power or supply or voltage) and chang\$5 near3 (voltage or potential or level) near3 (activat\$5 or select\$5)).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 19:24